Docket No.: 42P16958



## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

the Application of:

BLAISE B. FANNING

Application No.: 10/750,148

Filed: December 31, 2003

For: DEDICATED CACHE MEMORY

Art Group: 2186

Examiner: Lev Iwashko

## INFORMATION DISCLOSURE STATEMENT UNDER 37 C.F.R. §1.97

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

In accordance with the duty of disclosure, enclosed is a copy of IDS Citation Form PTO/SB/08 or PTO-1449, together with copies of the documents cited on that form, except for copies not required to be submitted (e.g., copies of U.S. patents and U.S. published patent applications need not be enclosed). This IDS and IDS Citation Form are being submitted concurrently with the Request for Continued Examination. It is respectfully requested that the cited references be considered and that the enclosed copy of PTO/SB/08 be initialed by the Examiner to indicate such consideration and a copy thereof returned to applicant(s).

The submission of this Information Disclosure Statement is not to be construed as a representation that a search has been made in the subject application and is not to be construed as an admission that the information cited in this statement is material to patentability.

Please charge any fees due to Deposit Account 02-2666. A duplicate copy of the Fee Transmittal (PTO/SB/17) is enclosed for this purpose.

Respectfully submitted,

BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP

Date: September 27, 2006

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I hereby certify that this correspondence is being deposited with the United States Postal Service on the date shown below with sufficient postage as first class mail in an envelope addressed to: Mail Stop RCE, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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-2- 42P16958

Substitute for form 1449A/PTO

## INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(use as many sheets as necessary)

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	Complete if Known
Application Number	10/750,148
Filing Date	December 31, 2003
First Named Inventor	Blaise B. Fanning
Art Unit	2186
Examiner Name	Lev Iwashko
Attorney Docket Number	42P16958

Earniner Initials*	Cite No.¹	Document Number  Number - Kind Code² (if known)	Publication Date or Issue Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		US-6625695	09-23-2003	FANNING	
		US-6523092	02-18-2003	FANNING	
		US-7017008	03-21-2006	FANNING	
		US-6904499	06-07-2005	FANNING	
		US-6516387	02-04-2003	AURACHER	
		US-			
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	FOREIGN PATENT DOCUMENTS							
Examiner	Cite	Foreign Patent Document			Bassa Calumna Linea	T.		
Initials*	No.1	Country Code <sup>3</sup> - Number <sup>4</sup> - Kind Code <sup>5</sup> (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear			
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Examiner	Date
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Based on PTO/SB/08A (08-03) as modified by Blakely, Solokoff, Taylor & Zafman (wtr) 08/11/2003.

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Substitute for form 1449A/PTO				Complete if Known		
INFORMATION DISCLOSURE				Application Number	10/750,148	
				Filing Date	December 31, 2003	
STATEMENT BY APPLICANT			CANT	First Named Inventor	Blaise B. Fanning	
				Art Unit	2186	
				Examiner Name	Lev Iwashko	
Sheet	eet 2	of	<b>If</b> 2	Attorney Docket Number	42P16958	

NON PATENT LITERATURE DOCUMENTS					
		Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T²		
	·	KIM et al - IEEE Transactions on VLSI, Vol. 9, No. 4, pp. 509-523 - Aug 2001 - A reconfigurable multi-function computing cache architecture			
		ZHANG et al - ACM Transactions on Embedded Computing Systems, Vol. 4, No. 2, May 2005, Pages 363-387 - A highly configurable cache for low energy embedded systems			
			*		

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